



KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE :: WARANGAL-15

(An Autonomous Institute under Kakatiya University, Warangal)

**VLSI&ES
PRR 20**

DEPARTMENT OF ELECTRONICS AND INSTRUMENTATION ENGINEERING

ONLINE CLASSWORK TIMETABLE for the ODD SEMESTER of Academic Year 2020-21

I SEMESTER of M.Tech (VLSI&ES)

Class Teacher: Sri O. Anjaneyulu

w.e.f. 01.02.2021

DAY / TIME	9.30 am - 10.30 am	10.45 am - 11.45am	12.00 pm -1.00pm	L U N C H B R E A K	2.00 pm - 3.00 pm	3.15 pm - 4.15 pm
MONDAY	MBES	STA	ESDWRTOS		RM&IPR	←----DICD-Lab--→ OA/GR
TUESDAY	DICD	ESDWRTOS	MBES		STA	ERPW
WEDNESDAY	ESDWRTOS	DICD	RM&IPR		MBES	ERPW
THURSDAY	STA	MBES	ERPW		DICD	-
FRIDAY	RM&IPR	DICD	STA		ESDWRTOS	←----MBES-Lab→ Dr.KSC/BKS
SATURDAY	-	-	-		-	-

Course Title in short	Course Code & Course Name	Name of the Course Faculty
DICD	: P20VE101- Digital IC Design	- Sri O. Anjaneyulu (OA)
MBES	: P20VE102- Microcontroller based Embedded Systems	- Dr. K. Srinivas (Dr.KSC)
STA	: P20VE103A- Static Timing Analysis	- Sri B. Jeevan (BJ)
ESDWRTOS	: P20VE104A- Embedded Systems Design with RTOS	- Smt. K. Shailaja (KSJ)
RM&IPR	: P20MC107- Research Methodology and IPR	- Sri G. Raju (GR)
ERPW	: P20AC108A- English for Research Paper Writing	- Sri B. Krishna Sundeep (BKS)
DICD-Lab	: P20VE105 - Digital IC Design Laboratory	- Sri O. Anjaneyulu / Sri G. Raju
MBES-Lab	: P20VE106 - Microcontroller based Embedded Systems Laboratory	- Dr. K. Srinivas/ Sri B. Krishna Sundeep

Sd/-
Faculty I/c, Dept. Time Tables

Sd/-
Faculty I/c, Overall Time Tables

Sd/-
Head of the Department

Sd/-
Dean, AA

Sd/-
Principal

